

JCG79 U.S. PTO
10/039707
11/09/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10039707	FILING DATE 11/09/2001	CLASS 710	SUBCLASS 310	GAU 2181 2134	EXAMINER Wong C. Lee 21/2
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**APPLICANTS: Jaramillo Ken; Wu Shih; Ahern Frank;

**CONTINUING DATA VERIFIED: *No NE CCL*

** FOREIGN APPLICATIONS VERIFIED: *No NE CCL*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <i>CCL</i> 5/28/04
ATTORNEY DOCKET NO 101950.00076		
TITLE : Method for increasing peripheral component interconnect (PCI) bus throughput via a bridge for memory read transfers via dynamic variable prefetch		
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg. Figs.Drwg. Print Fig.		
TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
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